



Silas Gallagher

Hardware Engineer I

Hardware engineer with 2 years of experience designing circuit boards, sensor interfaces, and microprocessor evaluation boards using VHDL, Python, and digital oscilloscopes. Completed 4 successful engineering projects for sensor interface boards, each meeting the design-verification test criteria on the first test cycle. Reduced development cycle time by 19 days on a peripheral controller redesign by automating signal-integrity checks.

CONTACT INFORMATION



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Tooele, UT 12345

KEY SKILLS

- VHDL
- FPGA platforms
- Python
- MATLAB
- Digital oscilloscopes
- Signal generators
- Temperature chambers
- Linux
- Git
- Design verification testing
- Circuit schematic review

PROFESSIONAL EXPERIENCE

HARDWARE ENGINEER I | BONNEVILLE MESA ELECTRONICS, TOOELE, UT 2025 – PRESENT

- Delivered a custom integrated circuit board project 11 days ahead of schedule by coordinating schematic review, layout, and test milestones across a 4-member team.
- Reduced development cycle time by 19 days on a peripheral controller redesign by automating signal-integrity checks with a Python script and digital oscilloscopes.
- Completed 4 successful engineering projects for sensor interface boards, each meeting the design-verification test criteria on the first test cycle.
- Evaluated cost constraints and security restrictions for 3 hardware configurations, selecting components that met cybersecurity system hardening requirements.

JUNIOR HARDWARE ENGINEER | STANSBURY RIDGE TECHNOLOGY, STANSBURY PARK, UT 2024 – 2025

- Designed support logic for a microprocessor evaluation board and delivered the project 6 days before the committed deadline.
- Saved 14 days of development cycle time by writing reusable VHDL testbench modules that eliminated manual waveform comparison across 8 signal groups.
- Executed environmental stress tests in temperature chambers for 3 successful board bring-ups, logging thermal data from negative 40 to 105 degrees Celsius.

EDUCATION

- BS in Computer Engineering, Utah Valley University, Orem, UT | 2020 – 2024