



Cookeville, TN 12345



(931) 555-0111



xavier.zamora@example.com

KEY SKILLS

- VHDL
- FPGA design
- Python
- MATLAB
- Digital oscilloscope operation
- Linux
- Git
- Schematic review
- Design verification testing
- RF test and measurement
- Hardware integration
- SAP ERP

Xavier Zamora

Computer Engineer

Bringing 6 years of experience in FPGA design, hardware testing, and production-level board integration across VHDL, Python, MATLAB, and SAP ERP environments. Redesigned a power distribution subsystem that reduced production costs by \$137,400 per year across two product lines, and authored 27 engineering documents including schematics, test plans, and integration guides.

PROFESSIONAL EXPERIENCE

COMPUTER ENGINEER

CALFKILLER CREEK ELECTRONICS, COOKEVILLE, TN | 2022 – PRESENT

- Redesigned a power distribution subsystem and switched to lower-cost voltage regulators, reducing production costs by \$137,400 per year across two product lines.
- Authored 27 engineering documents including schematics, test plans, and integration guides for FPGA-based control boards.
- Tracked 318 hardware inventory items in SAP ERP, reconciling physical counts quarterly and resolving discrepancies within 3 days.
- Tested and verified 14 peripheral interface boards using digital oscilloscopes, recording pass/fail data for each signal path against the test plan.

JUNIOR COMPUTER ENGINEER

OBEY RIVER TECHNOLOGY GROUP, SPARTA, TN | 2021 – 2022

- Wrote VHDL modules for a communications controller, producing 11 design documents that passed internal review on the first submission.
- Managed 174 hardware inventory items across 3 storage locations, keeping stock records current to within 48 hours of receipt.
- Analyzed user needs for a lab monitoring system and recommended a sensor board configuration that cut the bill of materials by \$8,740 per batch.

HARDWARE TEST TECHNICIAN

FALLING WATER SYSTEMS, MCMINNVILLE, TN | 2020 – 2021

- Ran environmental stress tests in temperature chambers for 9 board revisions, logging thermal performance data across a range of negative 40 to 85 degrees Celsius.
- Produced 5 engineering test reports per month summarizing signal integrity measurements captured with network analyzers.
- Updated personal skills in Python and MATLAB by completing 126 hours of vendor training, then automated a data-parsing script that cut manual entry time from 4.5 hours to 47 minutes per test cycle.

EDUCATION

BS in Computer Engineering

Tennessee Technological University, Cookeville, TN | 2016 – 2020